

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	238	((integrated adj circuit\$2) or circuit\$2) with (print adj head) with (support chassis brace frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/29 11:29
L2	26	"347"/\$.ccls. and ((print adj head) with (integrated near (circuit chip)) near5 (two multiple plurality))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/29 11:29
L3	5	"7021843"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:30
L5	21	347/42,13.ccls. and ((print adj head) with (circuit chip) with (multiple plurality))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/29 11:30
L7	3	("6439908").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:31
L8	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:32
S1	189	((integrated adj circuit\$2) or circuit\$2) with (print adj head) with (support chassis brace frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:29
S2	64140	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 15:42

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S3	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 09:54
S4	73	S1 and S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/30 15:42
S5	557	((two four eight sixteen) near15 circuit) with control\$3) and (print adj head)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13
S6	64198	("347").CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 09:56
S7	238	S6 and S5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 09:56
S8	142671	((two four eight sixteen) near15 circuit) with control\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13
S9	993	S8 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/01 10:13

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S10	25	("4423401" "4553393" "4672398" "4706130" "4737802" "4855567" "4864824" "4947192" "5029805" "5057854" "5107276" "5160945" "5218754" "5258774" "5412410" "5565900" "5665249" "5666141" "5719602" "5719604" "5812159" "5867183" "5896155" "6007187" "6312114").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:16
S11	127749	controller with circuit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:16
S12	4	S11 and S10	US-PGPUB; USPAT; USOCR	OR	OFF	2005/12/01 10:17
S13	0	("6916082").URPN.	USPAT	OR	OFF	2005/12/01 10:21
S14	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 09:19
S15	196	((integrated adj circuit\$2) or circuit\$2) with (print adj head) with (support chassis brace frame))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 09:33
S16	20	347/42,13.ccls. and ((print adj head) with (circuit chip) with (multiple plurality))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/29 11:30
S17	23	"347"/\$.ccls. and ((print adj head) with (integrated near (circuit chip)) near5 (two multiple plurality))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/29 11:29
S18	3	("6609787").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 09:56

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S19	2	("20050157052").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/27 10:49
S20	2	("7021843").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/15 13:46
S21	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/15 13:46
S22	3	("6439908").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:31
S23	2	("6916082").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/29 11:31
S24	2	"347"/\$.ccls. and ((two near5 integrat\$3 near5 circuit) with (single near controller)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 11:32
S26	2	("7021843").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/19 11:58
S27	2	("20050157052").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/19 11:58